AMENDMENTS TO THE CLAIMS

(IN FORMAT COMPLIANT WITH THE REVISED 37 CFR 1.121)

Please cancel claim 9 without prejudice and please add new claim 21.

1. (CURRENTLY AMENDED) An apparatus comprising:

a transceiver circuit comprising a multiplexer circuit and a plurality of bus input/outputs (I/Os), wherein said transceiver circuit is configured to directly couple (i) an analog signal to said bus I/Os with said multiplexer circuit when said bus I/Os are in a first state and (ii) a plurality of first digital signals to said bus I/Os with said multiplexer circuit when said bus I/Os are in a second state.

2. (ORIGINAL) The apparatus according to claim 1, wherein said transceiver circuit is further configured to directly couple a plurality of second digital signals to said bus I/Os when said bus I/Os are in a third state.

3. (ORIGINAL) The apparatus according to claim 2, wherein said apparatus further comprises a second circuit (i) coupled to said transceiver circuit and (ii) configured to present/receive said first and second digital signals.

4. (ORIGINAL) The apparatus according to claim 3, wherein:

said analog signal comprises an audio signal;

said transceiver circuit comprises a cellular telephone transceiver circuit;

said second circuit comprises a cellular telephone application specific integrated circuit (ASIC); and

said bus I/Os comprise a cellular telephone interconnect.

5. (ORIGINAL) The apparatus according to claim 1, wherein (i) said bus I/Os and (ii) said first digital signals are compliant with a Universal Serial Bus On-The-Go (USB OTG) protocol.

6. (CURRENTLY AMENDED) The apparatus according to claim 3, wherein said transceiver circuit is configured to determine said <u>first state, said second state, or said third state</u> ~~state~~ of said bus I/Os.

7. (CURRENTLY AMENDED) The apparatus according to claim 3, wherein said second circuit is configured to determine said <u>first state, said second state, or said third state</u> ~~state~~ of said bus I/Os.

8. (ORIGINAL) The apparatus according to claim 2, wherein said second digital signals are signals selected from a group consisting of Inter-IC (I^2C) protocol and Serial Peripheral Interface (SPI) protocol signals.

9. (CANCELED)

10. (ORIGINAL) The apparatus according to claim 2, wherein said transceiver circuit comprises an interface circuit configured to control said coupling in response to said first, second, and third states.

11. (ORIGINAL) The apparatus according to claim 1, wherein said first digital signals comprise signals compliant to a Universal Serial Bus (USB) protocol.

12. (ORIGINAL) The apparatus according to claim 3, wherein said apparatus is configured to communicate said first, second, and third states between said transceiver circuit and said second circuit via one or more of said plurality of second digital signals.

13. (ORIGINAL) The apparatus according to claim 3, wherein (i) said transceiver circuit comprises a physical layer

interface circuit and (ii) said second circuit comprises a broadband processor circuit.

14. (CURRENTLY AMENDED) An apparatus for alternately presenting/receiving an analog signal or a plurality of digital signals via a plurality of transceiver bus input/outputs (I/Os) comprising:

means for determining a state of said bus inputs/outputs ~~I/Os~~;

means for directly coupling said analog signal to said bus inputs/outputs ~~I/Os~~ with a multiplexer circuit when said bus inputs/outputs ~~I/Os~~ are in a first state; and

means for directly coupling said plurality of digital signals with said multiplexer circuit to said bus inputs/outputs ~~I/Os~~ when said bus inputs/outputs ~~I/Os~~ are in a second state.

15. (CURRENTLY AMENDED) A method for alternately presenting/receiving an analog signal or a plurality of first digital signals via a plurality of transceiver bus input/outputs (I/Os) comprising the steps of:

(A) determining a state of said bus inputs/outputs ~~I/Os~~;

(B) directly coupling said analog signal to said bus inputs/outputs ~~I/Os~~ with a multiplexer circuit when said bus inputs/outputs ~~I/Os~~ are in a first state; and

(C) directly coupling said plurality of first digital signals to said bus inputs/outputs ~~I/Os~~ with said multiplexer circuit when said bus inputs/outputs ~~I/Os~~ are in a second state.

16. (CURRENTLY AMENDED) The method according to claim 15, wherein said method further comprises the step of:

directly coupling a plurality of second digital signals to said bus inputs/outputs ~~I/Os~~ when said bus inputs/outputs ~~I/Os~~ are in a third state.

17. (CURRENTLY AMENDED) The method according to claim 16, wherein step (A) comprises determining said state of said bus inputs/outputs ~~I/Os~~ via a cellular telephone transceiver circuit.

18. (CURRENTLY AMENDED) The method according to claim 17, wherein step (A) comprises determining said state of said bus inputs/outputs ~~I/Os~~ via a cellular telephone application specific integrated circuit (ASIC).

19. (ORIGINAL) The method according to claim 15, wherein (i) said analog signal comprises an audio signal and (ii) said first digital signals comprise signals compliant to a Universal Serial Bus On-The-Go (USB OTG) standard.

20. (ORIGINAL) The method according to claim 18, wherein said method further comprises the step of:

communicating said state between said transceiver circuit and said ASIC via said second digital signals.

21. (NEW) The apparatus according to claim 10, wherein said interface circuit generates a control signal to configure said multiplexer circuit to (i) couple said analog signal to said bus I/Os and (ii) couple said plurality of first digital signals to said bus I/Os.